

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	569	"sample and hold circuit"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 13:44
L2	30716	(variable adj gain adj amplifier) vga	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 13:56
L3	1	2 with (first adj sampl\$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 13:46
L4	7	2 and (first adj sampl\$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 13:46
L5	171	(first adj sampl\$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 13:47
L6	188	(second adj sampl\$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 13:47
L7	87	5 with 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 13:47
L8	1	7 with 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 13:48
L9	3	2 and 6 and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 13:48

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L10	15	2 with (sampl\$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 14:02
L11	12	2 and (sampl\$3 adj switch) and (hold\$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 14:14
L12	7	2 and (sample adj switch) and (hold adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 14:17
L13	36	chou.in. and kuo-yu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 14:18
L14	4	13 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 14:32
L15	12	"sample-hold-circuit"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 14:37
L16	22084	(sample adj hold) near circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 14:37
L17	792	16 and (vga (variable adj gain adj amplifier))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 14:39
L18	27	17 and (sampl\$3 adj switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 14:40
L19	2	18 and (hold\$3 adj switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 14:39

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L20	2	(16 and (vga (variable adj gain adj amplifier))) and (sampl\$3 near switch) and (hold near switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 14:40
L21	2	18 and ((sampl\$3 adj pulse adj signal) or (hold\$3 adj pulse adj signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 15:07
L22	880	(sampl\$3 adj pulse adj signal) or (hold\$3 adj pulse adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 15:07
L23	6	22 and 17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/12 15:07